



STIC Search Report

EIC 2100

STIC Database Tracking Number: 162274

TO: Joseph Pan
Location: RND 2C15
Art Unit : 2135
Tuesday, May 03, 2005

Case Serial Number: 09/978381

From: David Holloway
Location: EIC 2100
RND 4B19
Phone: 2-3528

david.holloway@uspto.gov

Search Notes

Dear Examiner Pan,

Attached please find your search results for above-referenced case.
Please contact me if you have any questions or would like a re-focused search.

David

5/5/6 (Item 6 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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014017880 **Image available**
WPI Acc No: 2001-502094/200155
Related WPI Acc No: 2002-205158
XRPX Acc No: N01-372369

Configuration method for computer system involves sending
configuration command to devices on host bus giving range of addresses
associated with particular device on bus

Patent Assignee: MICRON TECHNOLOGY INC (MICR-N)

Inventor: PORTERFIELD A K

Number of Countries: 001 Number of Patents: 001

Patent Family:

| Patent No | Kind | Date | Applicat No | Kind | Date | Week |
|------------|------|----------|---------------|------|----------|----------|
| US 6275888 | B1 | 20010814 | US 97974374 | A | 19971119 | 200155 B |
| | | | US 2000524625 | A | 20000313 | |

Priority Applications (No Type Date): US 97974374 A 19971119; US 2000524625
A 20000313

Patent Details:

| Patent No | Kind | Lan Pg | Main IPC | Filing Notes |
|------------|------|--------|-------------|---------------------------------|
| US 6275888 | B1 | 12 | G06F-013/00 | Cont of application US 97974374 |

Abstract (Basic): US 6275888 B1

NOVELTY - Processor (62) sends **configuration** command to devices (66-70) e.g. Peripheral Device Interconnect (PCI) bridges, on host bus (64). Command includes range of addresses assigned to particular bus e.g. Second PCI Bus. If processor then transmits transaction request to address in given range only device to which address assigned will respond.

DETAILED DESCRIPTION - Bus device e.g. First PCI bridge, may include set of **configuration** registers storing its own **configuration** data and set of **shadow configuration registers** storing **configuration** data of other devices on the host bus.

USE - Method of configuring a computer system (claimed).

ADVANTAGE - Allows several PCI buses to be bridged direct to host bus instead of to main PCI bus bridged to host bus as in traditional hierarchical bus structures.

DESCRIPTION OF DRAWING(S) - Drawing shows a block diagram of a system using the described method.

Processor (62)

Host bus (64)

PCI bridges (66,68,70)

pp; 12 DwgNo 2/3

Title Terms: **CONFIGURATION** ; METHOD; COMPUTER; SYSTEM; SEND;

CONFIGURATION ; COMMAND; DEVICE; HOST; BUS; RANGE; ADDRESS; ASSOCIATE;
DEVICE; BUS

Derwent Class: T01

International Patent Class (Main): **G06F-013/00**

International Patent Class (Additional): **G06F-013/38**

File Segment: EPI

2/5/4 (Item 4 from file: 350)
DIALOG(R) File 350:Derwent WPIX
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014384606 **Image available**
WPI Acc No: 2002-205309/200226
Related WPI Acc No: 2001-502094; 2002-205302
XRPX Acc No: N02-156289

Computer system configuring method involves transmitting configuration data assigned to one peripheral component interconnect bridge to host bus, so that transmission of transaction request is determined

Patent Assignee: PORTERFIELD A K (PORT-I); MICRON TECHNOLOGY INC (MICR-N)

Inventor: PORTERFIELD A K

Number of Countries: 001 Number of Patents: 002

Patent Family:

| Patent No | Kind | Date | Applicat No | Kind | Date | Week |
|----------------|------|----------|---------------|------|----------|----------|
| US 20020016877 | A1 | 20020207 | US 97974374 | A | 19971119 | 200226 B |
| | | | US 2000524625 | A | 20000313 | |
| | | | US 2001930055 | A | 20010814 | |
| US 6587868 | B2 | 20030701 | US 97974374 | A | 19971119 | 200345 |
| | | | US 2000524625 | A | 20000313 | |
| | | | US 2001930055 | A | 20010814 | |

Priority Applications (No Type Date): US 97974374 A 19971119; US 2000524625 A 20000313; US 2001930055 A 20010814

Patent Details:

| Patent No | Kind | Lan Pg | Main IPC | Filing Notes |
|----------------|------|--------|-------------|---|
| US 20020016877 | A1 | 10 | G06F-013/00 | Cont of application US 97974374 Cont of application US 2000524625 Cont of patent US 6275888 |
| US 6587868 | B2 | | G06F-015/16 | Cont of application US 97974374 Cont of application US 2000524625 Cont of patent US 6275888 |

Abstract (Basic): US 20020016877 A1

NOVELTY - The write commands including configuration data representing a range of addresses assigned to PCI bridge (66), are transmitted to the host bus (64) from the processor (62). The processor transmits a transaction request to an address selected from the assigned addresses. The PCI bridge determines that the transaction request should not be transmitted based on the configuration data.

USE - For configuring computer system with peer-to-peer arrangement of computer buses.

ADVANTAGE - Arranges the computer bridges, without the hardware specific BIOS routines, to configure the computer system. Employs **shadow configuration registers** to make the physical peer-to-peer arrangement logically appear to the operating system as a traditional hierarchical arrangement.

DESCRIPTION OF DRAWING(S) - The figure shows the block diagram of computer system.

Processor (62)
Host bus (64)
PCI bridge (66)
pp; 10 DwgNo 2/3

Title Terms: COMPUTER; SYSTEM; METHOD; TRANSMIT; CONFIGURATION; DATA; ASSIGN; ONE; PERIPHERAL; COMPONENT; INTERCONNECT; BRIDGE; HOST; BUS; SO; TRANSMISSION; TRANSACTION; REQUEST; DETERMINE

Derwent Class: T01

International Patent Class (Main): G06F-013/00; **G06F-015/16**

International Patent Class (Additional): G06F-013/36

File Segment: EPI

| Set | Items | Description |
|------|-------------------------------------|--|
| S1 | 495 | (SHADOW? OR MIRROR? OR STORED OR SAVED OR RECORDED OR REFERENCE?) (3N) (PCR? ? OR PLATFORM() CONFIG? OR CONFIGURATION() REGISTER?) |
| S2 | 6 | S1 AND (ESS OR PLATFORM? OR CONFIGURATION? OR BIOS OR BOOT? ? OR HASH?) |
| S3 | 7600 | (SECOND OR 2ND OR ANOTHER OR HIDDEN OR ADDITIONAL OR TWO OR BACKUP OR RESERV? OR SPARE) (3N) (PCR OR PLATFORM() CONFIGURATION? OR CONFIGURATION() REGISTER?) |
| S4 | 1518 | S3 AND (SECUR? OR HASH? OR SCRAMBL? OR ENCRYPT? OR ENCIPHER? OR ENCYIPHER? OR CYPHER? OR MALICIOUS OR ILLEGAL OR INTRUDER? OR VIRUS OR TROJAN OR MALWARE) |
| S5 | 8201 | (MATCH? OR COMPAR? OR SAME? OR IDENTICAL? OR EQUAL? ? OR EQUIVALENT? OR CORRECT?) (3N) (PCR? ? OR PLATFORM() CONFIG? OR CONFIG? () REGISTER?) |
| S6 | 169 | (S4 OR S5) AND (BOOT? ? OR BIOS OR INITIALI? OR START?) |
| S7 | 123 | RD (unique items) |
| S8 | 99 | S7 NOT PY>2001 |
| S9 | 0 | S8 AND (MICROPROCESSOR? OR PROCESSOR? OR BIOS OR BOOT? ? OR BASIC() (INPUT OR IN() PUT) OR OPERATING() SYSTEM? OR OS MOTHERBOARD? OR PLATFORM() REGISTER?) |
| S10 | 177 | S4 AND S5 |
| S11 | 0 | S10 AND (BOOT? ? OR BIOS OR BASIC() INPUT) |
| File | 8: Ei Compendex(R) | 1970-2005/Apr W4 (c) 2005 Elsevier Eng. Info. Inc. |
| File | 35: Dissertation Abs Online | 1861-2005/Mar (c) 2005 ProQuest Info&Learning |
| File | 65: Inside Conferences | 1993-2005/May W1 (c) 2005 BLDSC all rts. reserv. |
| File | 2: INSPEC | 1969-2005/Apr W4 (c) 2005 Institution of Electrical Engineers |
| File | 94: JICST-EPlus | 1985-2005/Mar W3 (c) 2005 Japan Science and Tech Corp (JST) |
| File | 111: TGG Natl. Newspaper Index (SM) | 1979-2005/May 02 (c) 2005 The Gale Group |
| File | 6: NTIS | 1964-2005/Apr W3 (c) 2005 NTIS, Intl Cpyrght All Rights Res |
| File | 144: Pascal | 1973-2005/Apr W4 (c) 2005 INIST/CNRS |
| File | 34: SciSearch(R) Cited Ref Sci | 1990-2005/Apr W4 (c) 2005 Inst for Sci Info |
| File | 62: SPIN(R) | 1975-2005/Feb W1 (c) 2005 American Institute of Physics |
| File | 99: Wilson Appl. Sci & Tech Abs | 1983-2005/Mar (c) 2005 The HW Wilson Co. |
| File | 95: TEME-Technology & Management | 1989-2005/Mar W4 (c) 2005 FIZ TECHNIK |

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PCR-restriction fragment length polymorphism (RFLP) analyses ...

... Five fragments were found to be either monomorphic or otherwise problematic (producing **shadow PCR** fragments, failing to amplify many strains, etc.). ...

[www.blackwell-synergy.com/links/ doi/10.1046/j.1365-294X.1999.00523.x/abs/](http://www.blackwell-synergy.com/links/doi/10.1046/j.1365-294X.1999.00523.x/abs/) - [Similar pages](#)

Roland CY12RC V-Cymbal Ride/detail-Gibson J100 Xtra Acoustic ...

... **Shadow pcr** glad likes pennies familiar themselves frank pennies... Tabbed
behringers volule ahhhhh bis pyschological bis! Malicious ...

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| Set | Items | Description |
|-----|-------|--|
| S1 | 276 | (SHADOW? OR MIRROR? OR STORED OR SAVED OR RECORDED OR REFERENCE?) (3N) (PCR OR PLATFORM() CONFIGURATION OR CONFIGURATION() - REGISTER?) |
| S2 | 5 | S1 AND IC=G06F-015 |
| S3 | 39 | S1 AND (ESS OR PLATFORM? OR CONFIGURATION? OR BIOS OR BOOT? ? OR HASH?) |
| S4 | 36 | S3 NOT S2 |
| S5 | 20 | S4 AND IC=G06F |
| S6 | 1194 | (SECOND OR 2ND OR ANOTHER OR HIDDEN OR ADDITIONAL OR TWO OR BACKUP OR RESERV? OR SPARE) (3N) (PCR OR PLATFORM() CONFIGURATION? OR CONFIGURATION() REGISTER?) |
| S7 | 1 | S6 AND IC=G06F-015 |
| S8 | 55 | S6 AND IC=(G06F OR H04L OR H04K) |
| S9 | 4 | S8 AND (SECUR? OR HASH? OR SCRAMBL? OR ENCRYPT? OR ENCIPHER? OR ENCYIPHER? OR CYPHER? OR MALICIOUS OR ILLEGAL OR INTRUDER? OR VIRUS OR TROJAN OR MALWARE) |
| S10 | 508 | (MATCH? OR COMPAR? OR SAME? OR IDENTICAL? OR EQUAL? ? OR EQUIVALENT? OR CORRECT?) (3N) (PCR? ? OR PLATFORM() CONFIG? OR CONFIG?() REGISTER?) |
| S11 | 2 | S10 AND IC=G06F-015 |
| S12 | 48 | S10 AND IC=(G06F OR H04L OR H04K) |
| S13 | 47 | S12 NOT (S7 OR S9 OR S5) |
| S14 | 34 | S13 NOT AD>20011016 |

? show files

File 347:JAPIO Nov 1976-2004/Dec(Updated 050405)
(c) 2005 JPO & JAPIO

File 350:Derwent WPIX 1963-2005/UD,UM &UP=200527
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| Set | Items | Description |
|-----------|-------|--|
| S1 | 5 | SHADOW() (PCR OR PLATFORM() CONFIGURATION() REGISTER?) |
| File 340: | | CLAIMS(R)/US Patent 1950-05/Apr 28 |
| | | (c) 2005 IFI/CLAIMS(R) |
| File 349: | | PCT FULLTEXT 1979-2005/UB=20050428, UT=20050421 |
| | | (c) 2005 WIPO/Univentio |
| File 351: | | Derwent WPI 1963-2005/UD, UM & UP=200527 |
| | | (c) 2005 Thomson Derwent |
| File 654: | | US Pat.Full. 1976-2005/Apr 28 |
| | | (c) Format only 2005 The Dialog Corp. |

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| Examined 100 files | |
| Examined 150 files | |
| Examined 200 files | |
| 1 | 340: CLAIMS(R)/US Patent_1950-05/Apr 28 |
| 1 | 349: PCT FULLTEXT_1979-2005/UB=20050428,UT=20050421 |
| 1 | 351: Derwent WPI_1963-2005/UD,UM &UP=200527 |
| Examined 250 files | |
| Examined 300 files | |
| Examined 350 files | |
| Examined 400 files | |
| 2 | 654: US Pat.Full._1976-2005/Apr 28 |
| Examined 450 files | |
| Examined 500 files | |
| Examined 550 files | |

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May 03, 2005

USPTO

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